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# TITLE : HV133WX1-100

# Product Specification

# Rev. 0

## BOE HYDIS TECHNOLOGY

# REVISION HISTORY

| REV. | ECN NO. | DESCRIPTION OF CHANGES | DATE | PREPARED |
|---|---|---|---|---|
| 0 | | ▪ Initial Release | 08.05.28 | H.S. KIM |
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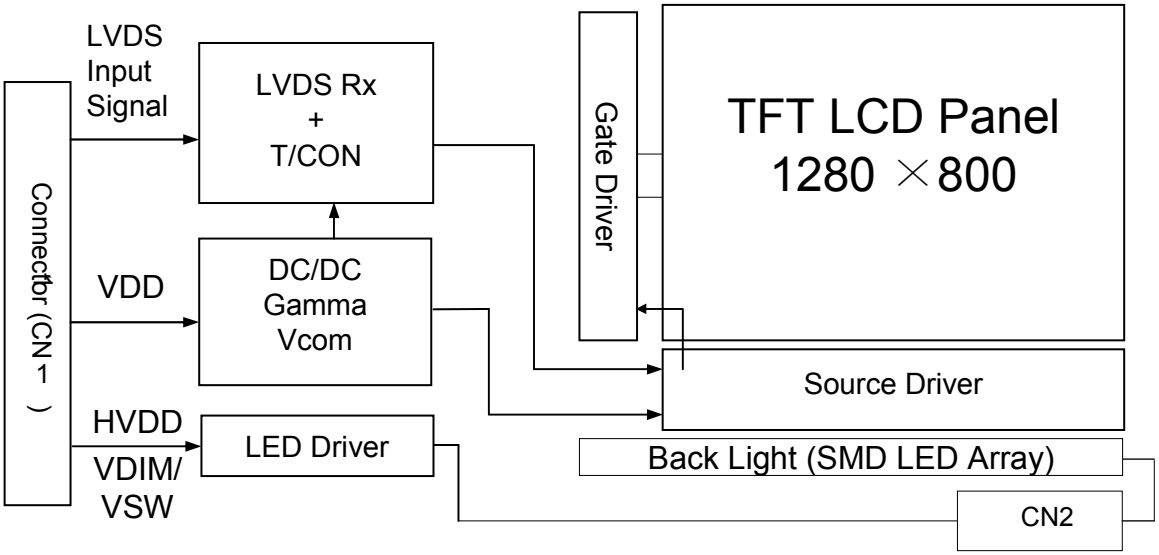
# 1.0 GENERAL DESCRIPTION

## 1.1 Introduction

HV133WX1-100 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 13.3 inch diagonally measured active area with WXGA resolutions (1280 horizontal by 800 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical Stripe and this module can display 262,144 colors. The TFT-LCD panel used for this module is a low reflection and higher color type.




## 1.2 Features

- Thin and Light Weight
- 3.3 V Logic Power Supply
- 12V Back-light Power Supply
- 1 Channel LVDS Interface
- SMD LED (54EA) Array (Bottom Side/Horizontal Direction)
- 262,144 Colors
- Data Enable Signal Mode
- On Board EDID chip
- Side Mounting Frame
- Green Product (RoHS)

### 1.3 Application

- Tablet PC (Wide type)

### 1.4 General Specifications

| Parameter | Specification | Unit | Remarks |
|---|---|---|---|
| Active area | 286.08(H) ×178.80(V) | mm | |
| Number of pixels | 1280(H) ×800(V) | pixels | |
| Pixel pitch | 0.2235(H) ×0.2235(V) | mm | |
| Pixel arrangement | RGB Vertical Stripe | | |
| Display colors | 262,144 | colors | |
| Display mode | Normally Black | | |
| Outline dimension | 301.4±0.3(H) ×196.5±0.3(V) ×6.5(D:Max.) | mm | Note 1 |
| Weight | 275(Typ.) ± 10 | g | Note 2 |
| Back-light | SMD LED Array (54EA : 9 series X 6 parallel) | | |

Note 1 : at PCB side
Note 2 : without digitizer

## 2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

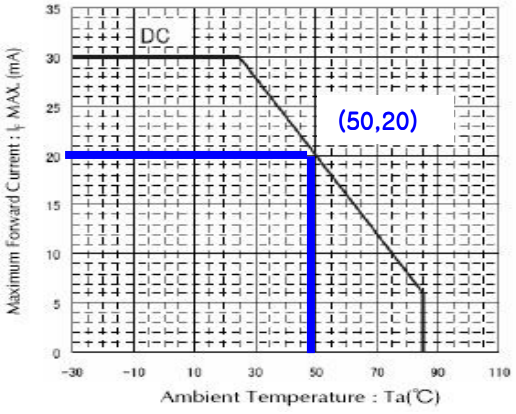
Ta=25+/-2°C

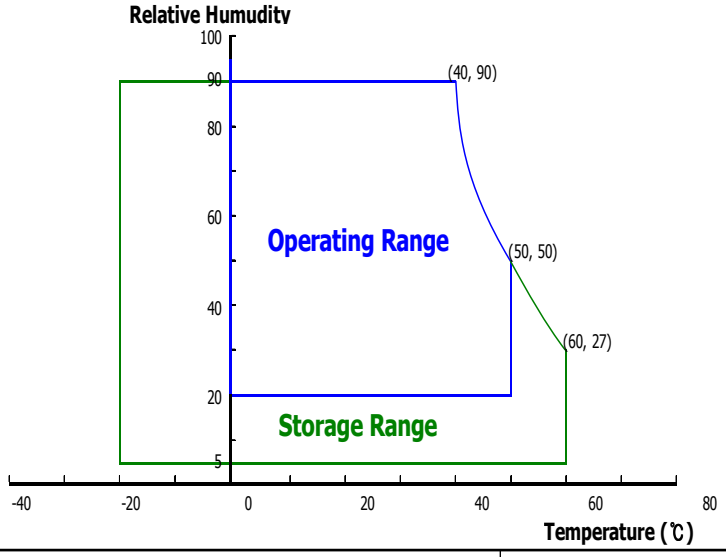
| Parameter | | Symbol | Min. | Max. | Unit | Remarks |
|---|---|---|---|---|---|---|
| Logic Power Supply Voltage | | $V_{DD}$ | -0.3 | 4.0 | V | |
| Logic Power Supply Voltage | | $V_{IN}$ | -0.3 | $V_{DD}$+0.3 | V | |
| Back-light Power Supply Voltage | | $HV_{DD}$ | -0.3 | 25 | V | |
| Back-light LED Current | + 25 ℃ | $I_{LED}$ | - | 30 | mA | Note 1 |
| | + 50 ℃ | $I_{LED}$ | - | 20 | mA | |
| Back-light LED Reverse Voltage | | $V_R$ | - | 5 | V | |
| Operating Temperature | | $T_{OP}$ | 0 | +50 | ℃ | Note 1, Note 2 |
| Storage Temperature | | $T_{SP}$ | -20 | +60 | ℃ | |

Note 1. Ambient temperature vs maximum forward current are shown in the figure below.

Note 2. Temperature and relative humidity range are shown in the figure below.
90% RH Max. ( 40℃ ≥ Ta)
Maximum wet - bulb temperature at 39℃ or less. ( > 40℃) No condensation.

Ambient Temperature vs Maximum Forward Current



# 3.0 ELECTRICAL SPECIFICATIONS

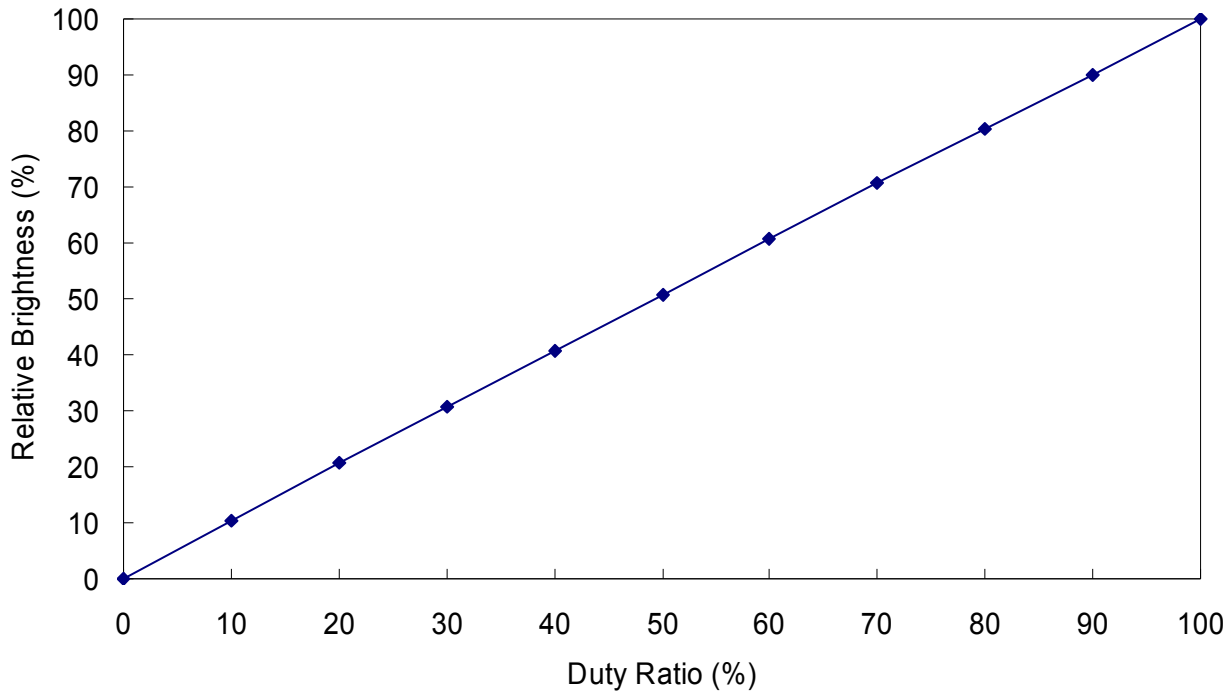
## 3.1 Electrical Specifications

< Table 3. Electrical Specifications >

| Parameter | | Min. | Typ. | Max. | Unit | Remarks |
|---|---|---|---|---|---|---|
| Logic Power Supply Voltage | $V_{DD}$ | 3.0 | 3.3 | 3.6 | V | Note 1 |
| Logic Power Supply Current | $I_{DD}$ | - | 360 | 550 | mA | Note 1 |
| Back-light Power Supply Voltage | $HV_{DD}$ | 5.0 | 12.0 | 21.0 | V | Note 2 |
| Back-light Power Supply Current | $I_{HVDD}$ | - | 271 | 325 | mA | Note 2, 3 |
| Back-light Power Consumption | $P_{BL}$ | - | 3.25 | 3.90 | W | Note 2, 3 |
| Power Consumption (EBL) | $P_{EBL}$ | - | 1.85 | 1.98 | W | Note 1, 2, 3 |
| LED Driver's Efficiency | $\eta$ | 80 | 85 | - | % | Note 2, 3 |
| Back-light PWM Frequency | $F_{PWM}$ | 200 | 280 | 350 | Hz | |
| High Level PWM Signal Voltage | $V_{PWMH}$ | 2.2 | 3.3 | 5.0 | V | |
| Low Level PWM Signal Voltage | $V_{PWML}$ | - | 0 | 0.2 | V | |
| High Level Differential Input Signal Voltage | $V_{IH}$ | - | - | +100 | mV | $V_{CM}$ = 1.2V |
| Low Level Differential Input Signal Voltage | $V_{IL}$ | -100 | - | - | mV | $V_{CM}$ = 1.2V |
| Back-light LED Voltage / Back-light LED Total Voltage | $V_{LED}$ /$V_{BL}$ | - | 3.2 / 28.8 | 3.6 /32.4 | V | Note 4 |
| Back-light LED Current / Back-light LED Total Current | $I_{LED}$ /$I_{BL}$ | - | 16.0 / 96.0 | 16.8 /100.8 | mA | Note 4 |
| Life Time | | 12,000 | - | - | Hrs | Based on LED 25'c, $I_{LED}$=16mA |
| Power Consumption | $P_D$ | - | 1.2 | 1.82 | W | Note 1 |
| | $P_{LED}$ | - | 2.76 | 3.27 | W | Note 4 |
| | $P_{total}$ | - | 3.96 | 5.09 | W | Note 1, 4 |

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM.
The current draw and power consumption specified is for 3.3V at 25℃.
a) Typ : Window XP pattern, b) Max : Skip Sub Pixel
c) EBL : Mosaic pattern ( 32 X 32 )
2. The power supply voltage and current is measured and specified at the interface connector of LCM including LED Driver.
3. Reference value, which is measured with LED Driver for 12V.
4. Reference value, which is measured without LED Driver.
5. Calculated value for reference ($V_{LED}$ × $I_{LED}$ × # of LEDs (54EA) ).

### 3.2 PWM Duty Ratio vs Brightness




Notes :

In case of duty ratio 0%, LED can't illuminate itself so this state is LED off.

In case of duty ratio 100%, the brightness of LED is maximum and the state is LED on.

# 4.0 OPTICAL SPECIFICATIONS

## 4.1 Overview

The test of optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25±2℃) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5A) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of Θ and Φ equal to 0°. We refer to $\Theta_{Ø=0}$ (=Θ3 ) as the 3 o'clock direction (the "right"), $\Theta_{Ø=90}$ (= Θ12 ) as the 12 o'clock direction ("upward"), $\Theta_{Ø=180}$ (= Θ9 ) as the 9 o'clock direction ("left") and $\Theta_{Ø=270}$(= Θ6 ) as the 6 o'clock direction ("bottom"). While scanning Θ and/or Ø, the center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. $V_{DD}$ shall be 3.3+/- 0.3V at 25°C. Optimum viewing angle direction is 6 o'clock.

## 4.2 Optical Specifications

<Table 4. Optical Specifications>

| Parameter | | Symbol | Condition | Min. | Typ. | Max. | Unit | Remarks |
|---|---|---|---|---|---|---|---|---|
| Viewing Angle range | Horizontal | $\Theta_3$ | CR > 10 | - | 89 | 90 | Deg. | Note 1 |
| | | $\Theta_9$ | | - | 89 | 90 | Deg. | |
| | Vertical | $\Theta_{12}$ | | - | 89 | 90 | Deg. | |
| | | $\Theta_6$ | | - | 89 | 90 | Deg. | |
| Luminance Contrast ratio | | CR | Θ = 0° | 400 | 500 | - | | Note 2 |
| Luminance of White | 1 Point | $Y_w$ | Θ = 0° | 250 | 300 | - | cd/m² | Note 4 Note 5 |
| White Luminance uniformity | 5 Points | ΔY5 | | 80 | - | - | % | |
| | 13 Points | ΔY13 | | 60 | - | - | | |
| White Chromaticity | | $W_x$ | Θ = 0° | 0.260 | 0.300 | 0.340 | | Note 3 |
| | | $W_y$ | | 0.280 | 0.320 | 0.360 | | |
| Reproduction of color | Red | $R_x$ | Θ = 0° | 0.523 | 0.563 | 0.603 | | |
| | | $R_y$ | | 0.314 | 0.354 | 0.394 | | |
| | Green | $G_x$ | | 0.291 | 0.331 | 0.371 | | |
| | | $G_y$ | | 0.502 | 0.542 | 0.582 | | |
| | Blue | $B_x$ | | 0.106 | 0.146 | 0.186 | | |
| | | $B_y$ | | 0.077 | 0.117 | 0.157 | | |
| Response Time | | Total ($T_r + T_d$) | Ta= 25° C Θ = 0° | - | 30 | - | ms | Note 6 |
| Cross Talk | | CT | Θ = 0° | - | - | 2.0 | % | Note 7 |

Notes :

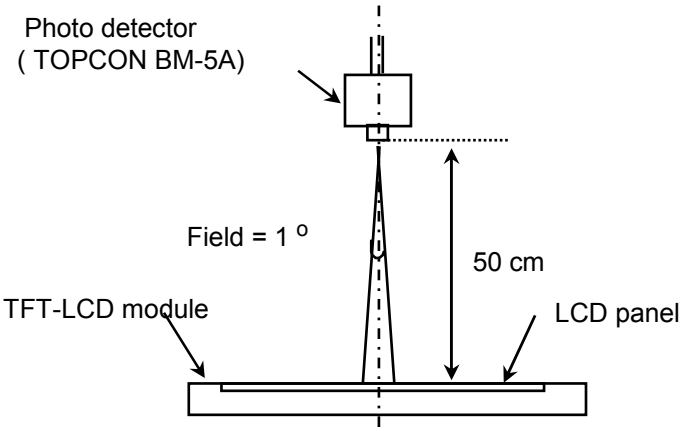
1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see Figure1).

2. Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state (see Figure1). Luminance Contrast Ratio (CR) is defined mathematically as CR = Luminance when displaying a white raster / Luminance when displaying a black raster.

3. Reference only / Standard Front Surface Treatment Measured with green cover glass. The color chromaticity coordinates specified in Table 4 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

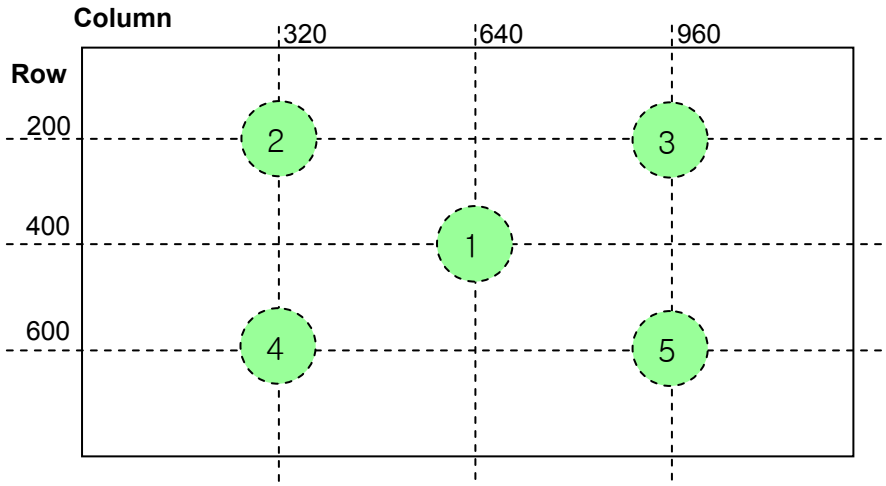
### 4.3 Optical Measurements

**Figure 1. Measurement Set Up**




Optical characteristics measurement setup

**Figure 2. Uniformity Measurement Locations (5 points)**

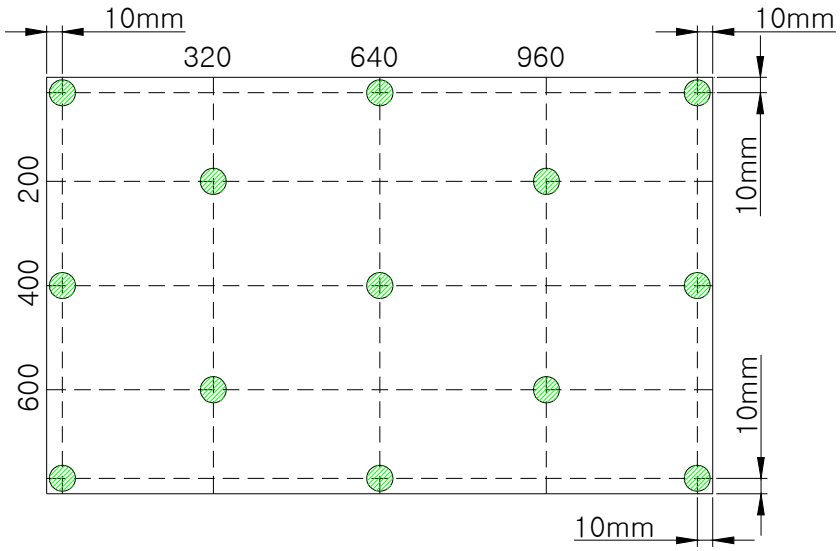



Note 4.

Luminance of white is defined as luminance value of the center across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in Figure 2 for a total of the measurements per display.

* Yw = Luminance of the panel center

**Figure 3. Uniformity Measurement Locations (13 points)**




Note 5.
The White luminance uniformity on LCD surface is then expressed as : ΔY5 = (Minimum Luminance of five points / Maximum Luminance of five points) X 100% (see Figure 2) , ΔY13 = (Minimum Luminance of 13 points /Maximum Luminance of 13 points) X 100% (see Figure 3).

**Figure 4. Response Time Testing**

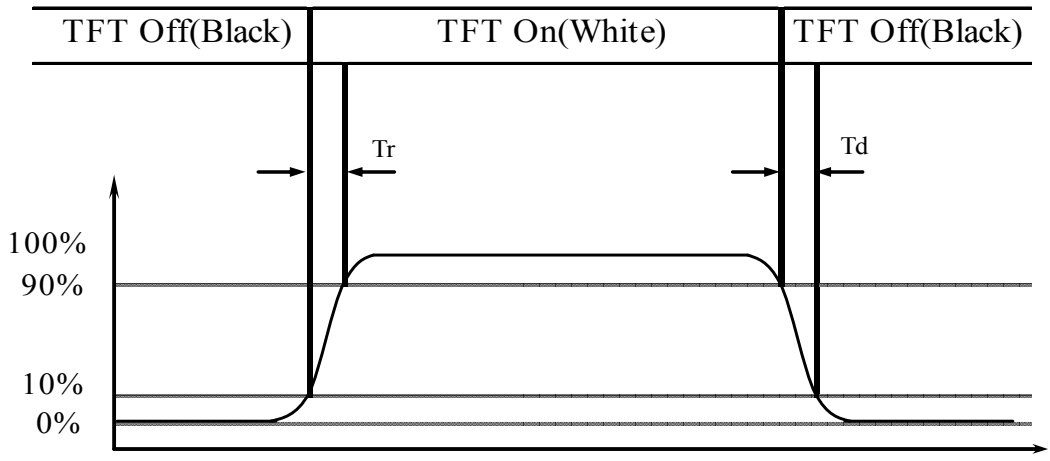
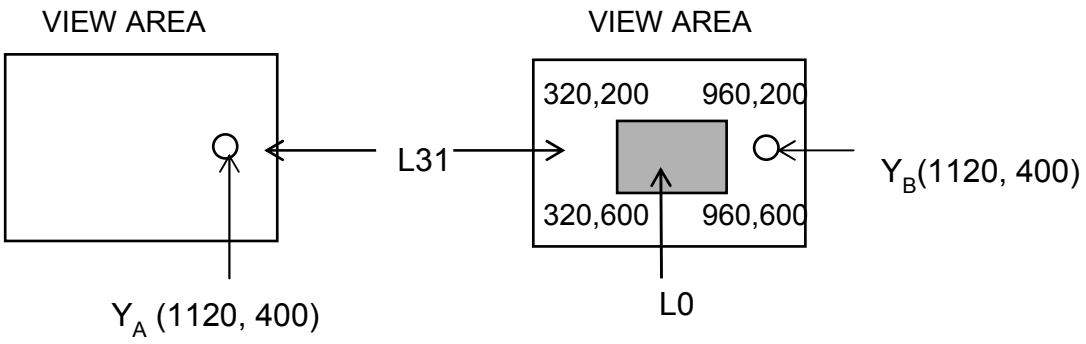


**Figure 5. Cross Modulation Test Description**




$$\text{Cross-Talk (\%)} = \left| \frac{Y_B - Y_A}{Y_B} \right| \times 100$$

Where:

$Y_A$ = Initial luminance of measured area (cd/m$^2$)

$Y_B$ = Subsequent luminance of measured area (cd/m$^2$)

The location measured will be exactly the same in both patterns

Note 6.

The electro-optical response time measurements shall be made as Figure 4 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.

Note 7.

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark (Refer to Figure 5).

# 5.0 INTERFACE CONNECTIONS

## 5.1 Electrical Interface Connection

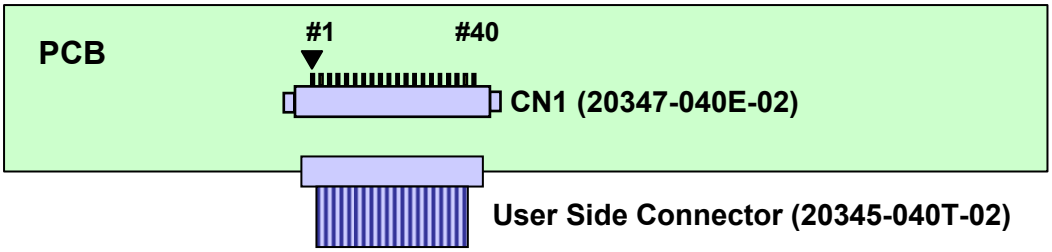
**CN1 Interface Connector (20347-040E-02 or Equivalent , Manufactured by I-PEX)**

**User Side Connector (20345-040T-02 or Equivalent , Manufactured by I-PEX)**

| Pin No. | Symbol | Function | Pin No. | Symbol | Function |
|---|---|---|---|---|---|
| 1 | GND1 | GROUND | 21 | GND6 | GROUND |
| 2 | NC | NON-CONNECTION | 22 | RCLKIN- | LVDS Negative clock signal (-) |
| 3 | VDD1 | Logic Power Supply : +3.3V | 23 | RCLKIN+ | LVDS Positive clock signal (+) |
| 4 | VDD2 | Logic Power Supply : +3.3V | 24 | GND7 | GROUND |
| 5 | VDD3 | Logic Power Supply : +3.3V | 25 | VDIM | PWM Brightness Control |
| 6 | VDD_DEID | EDID Power Supply : +3.3V | 26 | VSW | LED On/Off Control |
| 7 | NC | NON-CONNECTION | 27 | NC | NON-CONNECTION |
| 8 | CLK_EDID | EDID Clock | 28 | HVGND1 | GROUND |
| 9 | DATA_EDID | EDID Data | 29 | HVGND2 | GROUND |
| 10 | GND2 | GROUND | 30 | HVGND3 | GROUND |
| 11 | GND3 | GROUND | 31 | HVGND4 | GROUND |
| 12 | NC | NON-CONNECTION | 32 | HVGND5 | GROUND |
| 13 | RIN0- | LVDS Negative data signal (-) | 33 | NC | NON-CONNECTION |
| 14 | RIN0+ | LVDS Positive data signal (+) | 34 | HVDD1 | Back-light Power Supply: +12V |
| 15 | GND4 | GROUND | 35 | HVDD2 | Back-light Power Supply: +12V |
| 16 | RIN1- | LVDS Negative data signal (-) | 36 | HVDD3 | Back-light Power Supply: +12V |
| 17 | RIN1+ | LVDS Positive data signal (+) | 37 | HVDD4 | Back-light Power Supply: +12V |
| 18 | GND5 | GROUND | 38 | HVDD5 | Back-light Power Supply: +12V |
| 19 | RIN2- | LVDS Negative data signal (-) | 39 | NC | NON-CONNECTION |
| 20 | RIN2+ | LVDS Positive data signal (+) | 40 | NC | NON-CONNECTION |

Note 1. Start from left side

Note 2. Pin No. 26 (VSW) : Tie to OPEN (or VDD/+5V) for backlight ON or to GND for backlight OFF



### 5.2 LVDS Interface

**LVDS Transmitter : THC63LVDM83A**

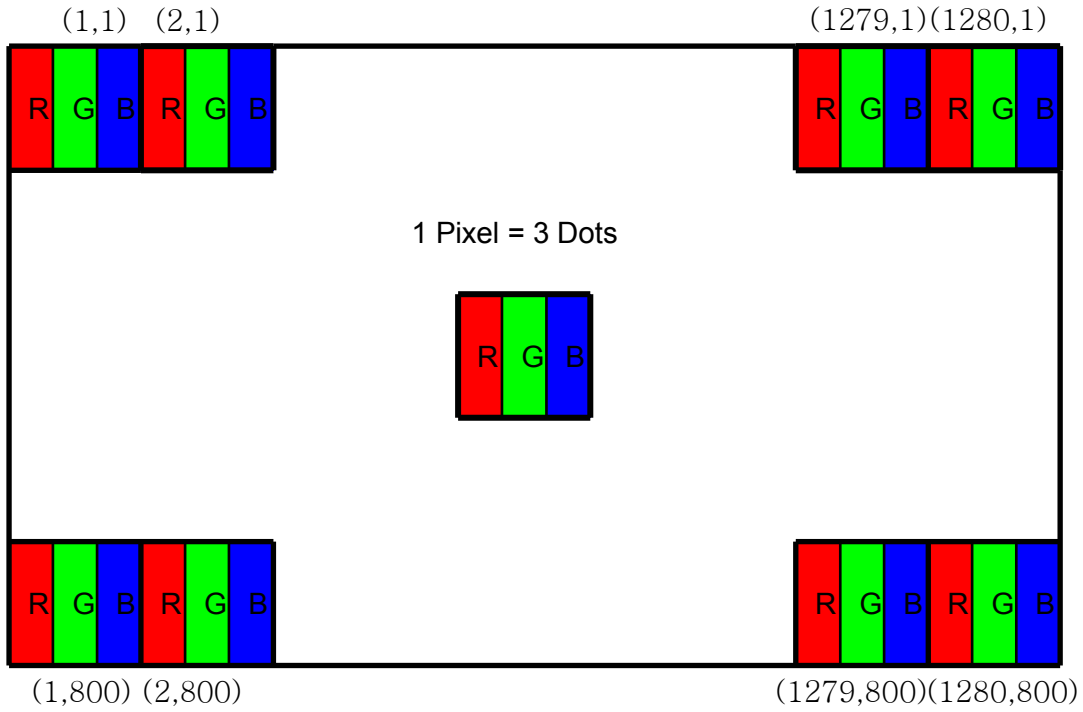
| Input signal | Transmitter | | Interface | | 20347-040E-02 | Remark |
|---|---|---|---|---|---|---|
| | Pin No | Pin No | System (Tx) | TFT-LCD (Rx) | Pin No. | |
| R0 | 51 | 48<br>47 | OUT0-<br>OUT0+ | IN0-<br>IN0+ | 13<br>14 | |
| R1 | 52 | | | | | |
| R2 | 54 | | | | | |
| R3 | 55 | | | | | |
| R4 | 56 | | | | | |
| R5 | 3 | | | | | |
| G0 | 4 | | | | | |
| G1 | 6 | 46<br>45 | OUT1-<br>OUT1+ | IN1-<br>IN1+ | 16<br>17 | |
| G2 | 7 | | | | | |
| G3 | 11 | | | | | |
| G4 | 12 | | | | | |
| G5 | 14 | | | | | |
| B0 | 15 | | | | | |
| B1 | 19 | | | | | |
| B2 | 20 | 42<br>41 | OUT2-<br>OUT2+ | IN2-<br>IN2+ | 19<br>20 | |
| B3 | 22 | | | | | |
| B4 | 23 | | | | | |
| B5 | 24 | | | | | |
| HSYNC | 27 | | | | | |
| VSYNC | 28 | | | | | |
| DE | 30 | | | | | |
| MCLK | 31 | 40 | CLKOUT- | CLKIN- | 22 | |
| | | 39 | CLKOUT+ | CLKIN+ | 23 | |

### 5.3 Back-light Interface

**CN2 LED FPC Connector (20397-008E, Manufactured by I-PEX)**

| Pin No. | Symbol | Function | Remark |
|---|---|---|---|
| 1 | Anode | LED Anode Power Supply | LED Anode Power Supply (3.2V X 9EA = 28.8V) |
| 2 | NC | Non-Connection | |
| 3 | Cathode1 | LED Cathode Power Supply | LED Cathode Power Supply |
| 4 | Cathode2 | LED Cathode Power Supply | |
| 5 | Cathode3 | LED Cathode Power Supply | |
| 6 | Cathode4 | LED Cathode Power Supply | |
| 7 | Cathode5 | LED Cathode Power Supply | |
| 8 | Cathode6 | LED Cathode Power Supply | |

### 5.4 Data Input Format



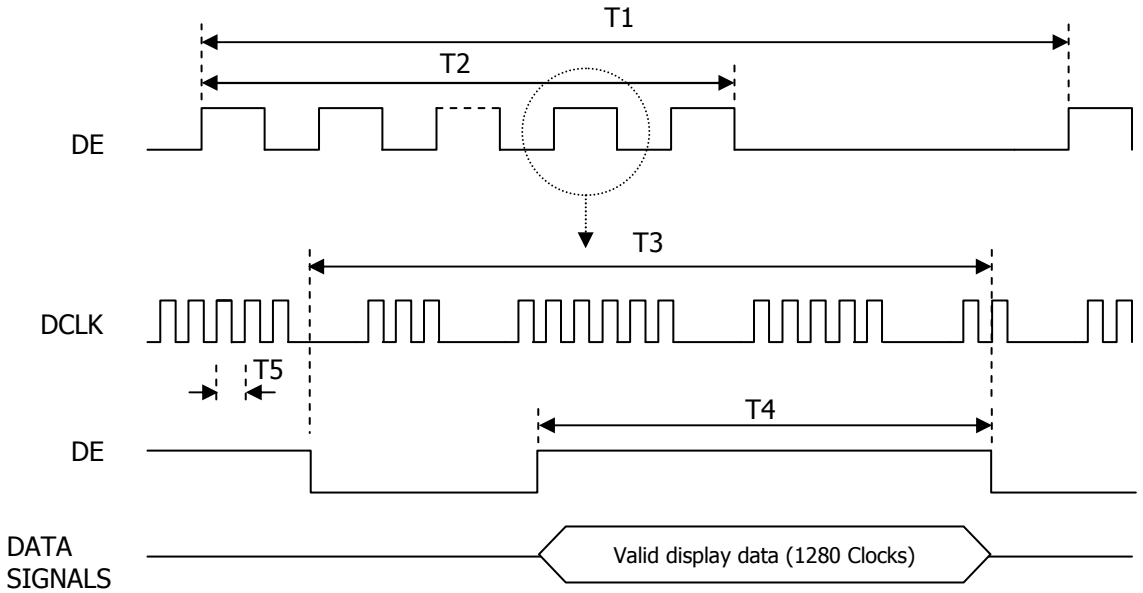
## 6.0. SIGNAL TIMING SPECIFICATIONS

### 6.1 The 13.3" WXGA LCM is operated by the only DE (Data enable) mode (LVDS Transmitter Input)

| Item | Symbol | Min. | Typ. | Max. | Unit |
|---|---|---|---|---|---|
| Frame Period | T1 | 810 | 823 | 840 | Lines |
| Vertical Display Period | T2 | - | 800 | - | Lines |
| One line Scanning Period | T3 | 1430 | 1440 | 1470 | Clocks |
| Horizontal Display Period | T4 | - | 1280 | - | Clocks |
| Clock Frequency | 1/T5 | 50 | 71.11 | 80 | MHz |

## 7.0 SIGNAL TIMING WAVEFORMS

### 7.1 Timing Waveforms of Interface Signal

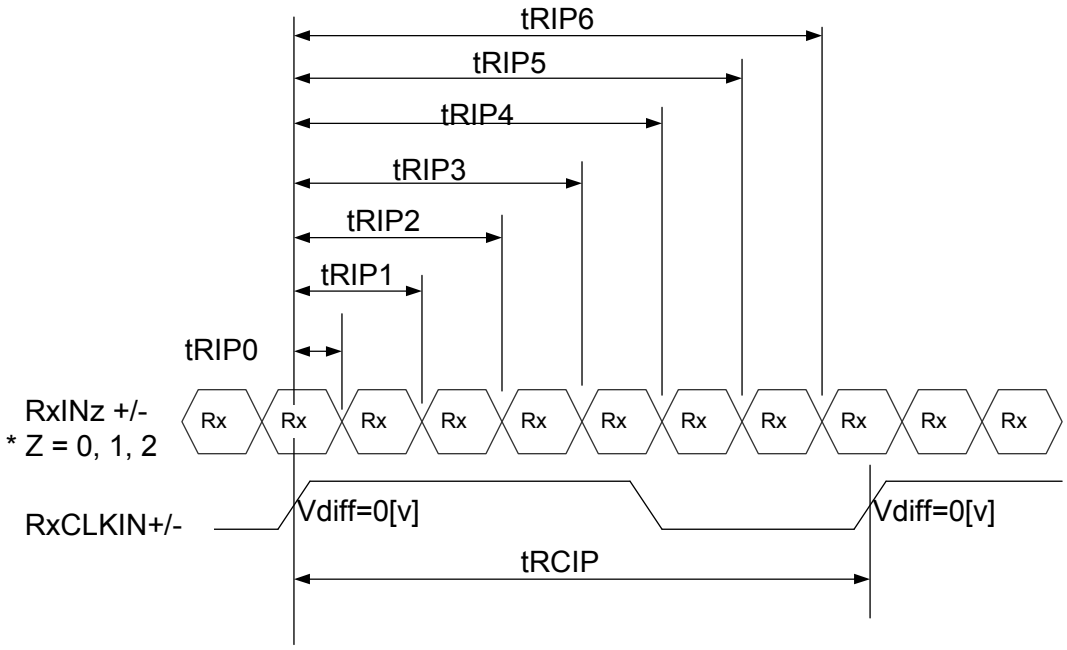


### 7.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter

< LVDS Rx Interface Timing Specification>

| Item | Symbol | Min. | Typ. | Max. | Unit | Remarks |
|---|---|---|---|---|---|---|
| CLKIN Period | tRCIP | 12.50 | 14.06 | 20.00 | nsec | |
| Input Data 0 | tRIP0 | -0.4 | 0.0 | +0.4 | nsec | |
| Input Data 1 | tRIP1 | tRICP/7-0.4 | tRICP/7 | tRICP/7+0.4 | nsec | |
| Input Data 2 | tRIP2 | 2 ×tRICP/7-0.4 | 2 ×tRICP/7 | 2 ×tRICP/7+0.4 | nsec | |
| Input Data 3 | tRIP3 | 3 ×tRICP/7-0.4 | 3 ×tRICP/7 | 3 ×tRICP/7+0.4 | nsec | |
| Input Data 4 | tRIP4 | 4 ×tRICP/7-0.4 | 4 ×tRICP/7 | 4 ×tRICP/7+0.4 | nsec | |
| Input Data 5 | tRIP5 | 5 ×tRICP/7-0.4 | 5 ×tRICP/7 | 5 ×tRICP/7+0.4 | nsec | |
| Input Data 6 | tRIP6 | 6 ×tRICP/7-0.4 | 6 ×tRICP/7 | 6 ×tRICP/7+0.4 | nsec | |



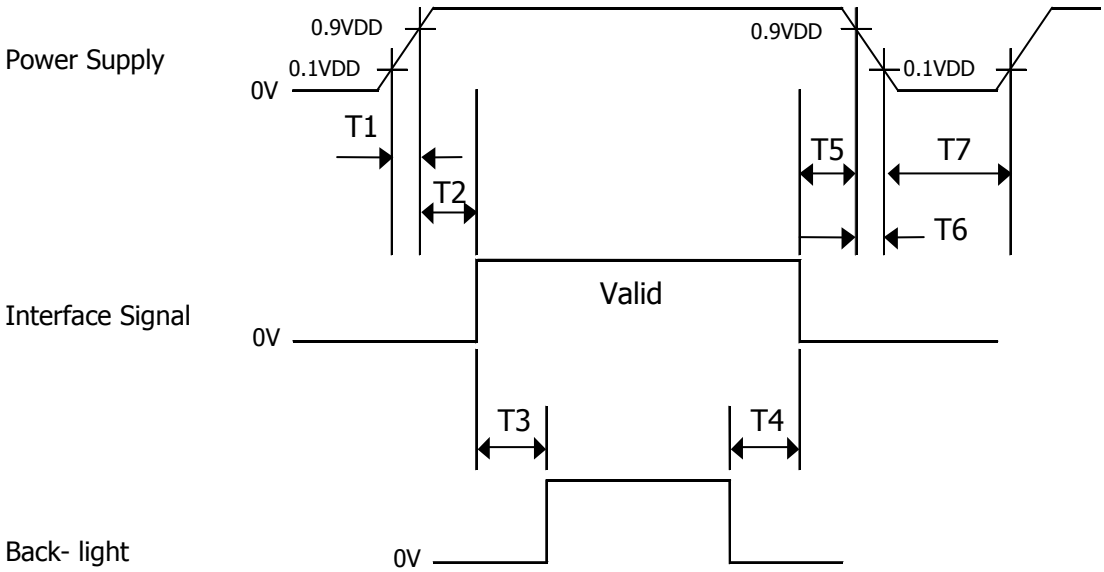
## 8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

Each color is displayed in sixty-four gray scales from a 6 bit data signal input. A total of 262,144 colors are derived from the resultant 18 bit data.

| Colors & Gray Scale | | Red Data | | | | | | Green Data | | | | | | Blue Data | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | | R5 | R4 | R3 | R2 | R1 | R0 | G5 | G4 | G3 | G2 | G1 | G0 | B5 | B4 | B3 | B2 | B1 | B0 |
| Basic Colors | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Green | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Cyan | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Red | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Magenta | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Yellow | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Gray Scale Of Red | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | △ | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Darker | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | △ | ↓ | | | | | | ↓ | | | | | | ↓ | | | | | |
| | ▽ | ↓ | | | | | | ↓ | | | | | | ↓ | | | | | |
| | Brighter | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | ▽ | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Scale Of Green | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | △ | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Darker | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | △ | ↓ | | | | | | ↓ | | | | | | ↓ | | | | | |
| | ▽ | ↓ | | | | | | ↓ | | | | | | ↓ | | | | | |
| | Brighter | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | ▽ | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Scale Of Blue | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | △ | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| | Darker | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| | △ | ↓ | | | | | | ↓ | | | | | | ↓ | | | | | |
| | ▽ | ↓ | | | | | | ↓ | | | | | | ↓ | | | | | |
| | Brighter | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 |
| | ▽ | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 |
| | Blue | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| Gray Scale Of White & Black | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | △ | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 |
| | Darker | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| | △ | ↓ | | | | | | ↓ | | | | | | ↓ | | | | | |
| | ▽ | ↓ | | | | | | ↓ | | | | | | ↓ | | | | | |
| | Brighter | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 |
| | ▽ | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 0 |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |

## 9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below




- T1 ≤ 10 ms
- 0 ≤ T2 ≤ 50 ms
- 200 ms ≤ T3
- 200 ms ≤ T4
- 0 ≤ T5 ≤ 50 ms
- 0 ≤ T6 ≤ 10ms
- 200ms ≤ T7

Notes : 1. When the power supply VDD is 0V, Keep the level of input signals on the low or keep high impedance.
2. Do not keep the interface signal high impedance when power is on.
3. Back Light must be turn on after power for logic and interface signal are valid.

# 10.0 MECHANICAL CHARACTERISTICS

## 10.1 Dimensional Requirements

Figure 6 & 7 (located in 11.0) shows mechanical outlines for the model

| Parameter | Specification | Unit |
|---|---|---|
| Active Area | 286.08(H) ×178.80(V) | mm |
| Number of pixels | 1280(H) X 800(V) (1 pixel = R + G + B dots) | |
| Pixel pitch | 0.2235(H) X 0.2235(V) | |
| Pixel arrangement | RGB Vertical stripe | |
| Display colors | 262,144 | |
| Display mode | Normally Black | |
| Outline dimension | 301.4±0.3(H) ×196.5±0.3(V) ×6.5(D:Max.) | mm |
| Weight | 275(Typ.) ± 10 | g |
| Back-light | SMD LED (54EA) Array | |

## 10.2 Mounting

See Figure 6 & 7 & 8. (shown in 11.0)

| Parameter | Specification | Unit |
|---|---|---|
| Torque of side mounting screw | 2.5(Max.) | kgf |

## 10.3 Glare and Polarizer Hardness.

The surface of the LCD has an glare coating to minimize reflection and a coating to reduce scratching.

## 10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 150lux. The manufacture shall furnish limit samples of the panel showing the light leakage acceptable.

## 11.0 Mechanical Drawing

**Figure 6. TFT-LCD Module Outline Dimension (Front View)**

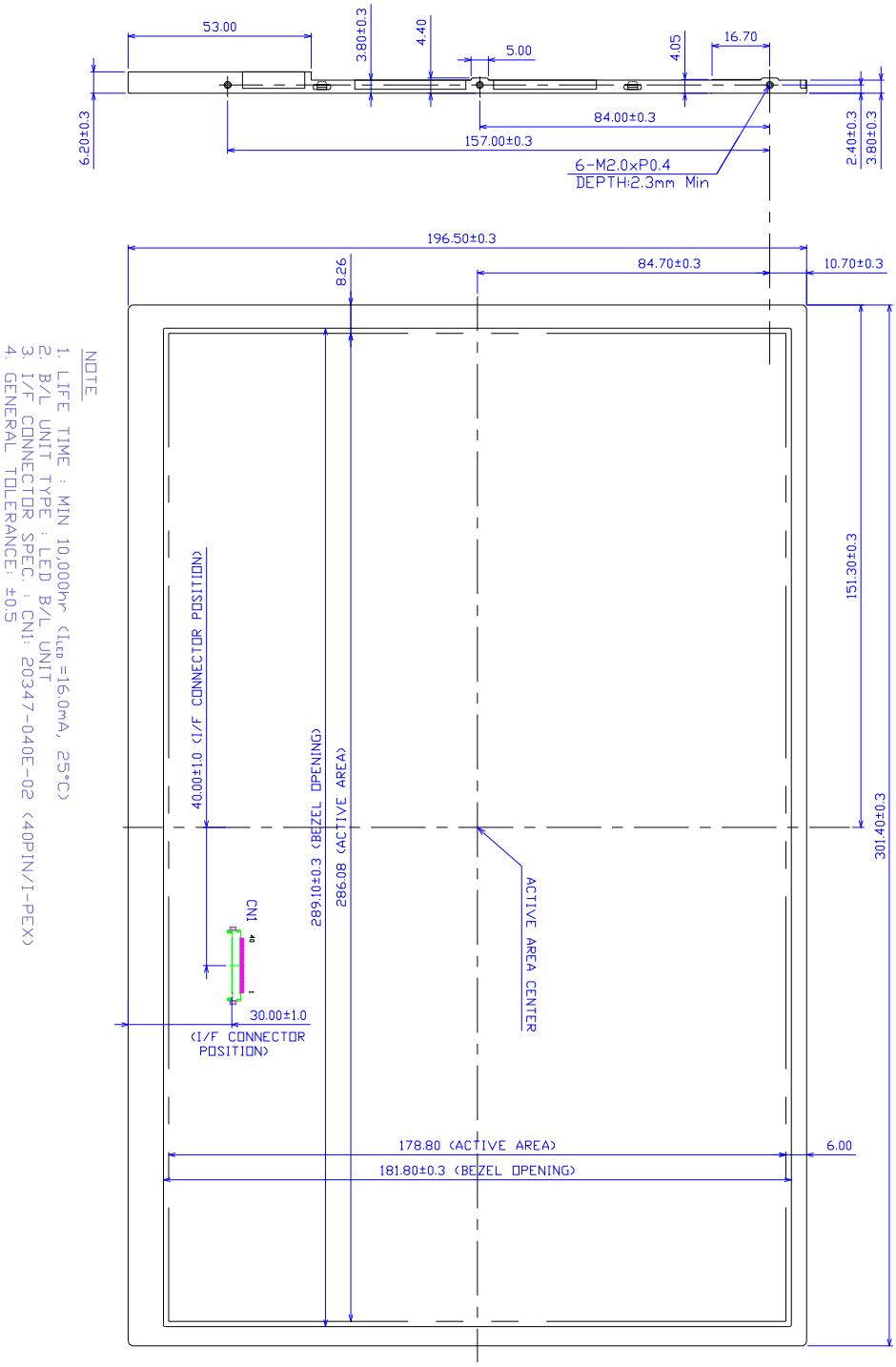


**Figure 7. TFT-LCD Module Outline Dimensions (Rear view)**

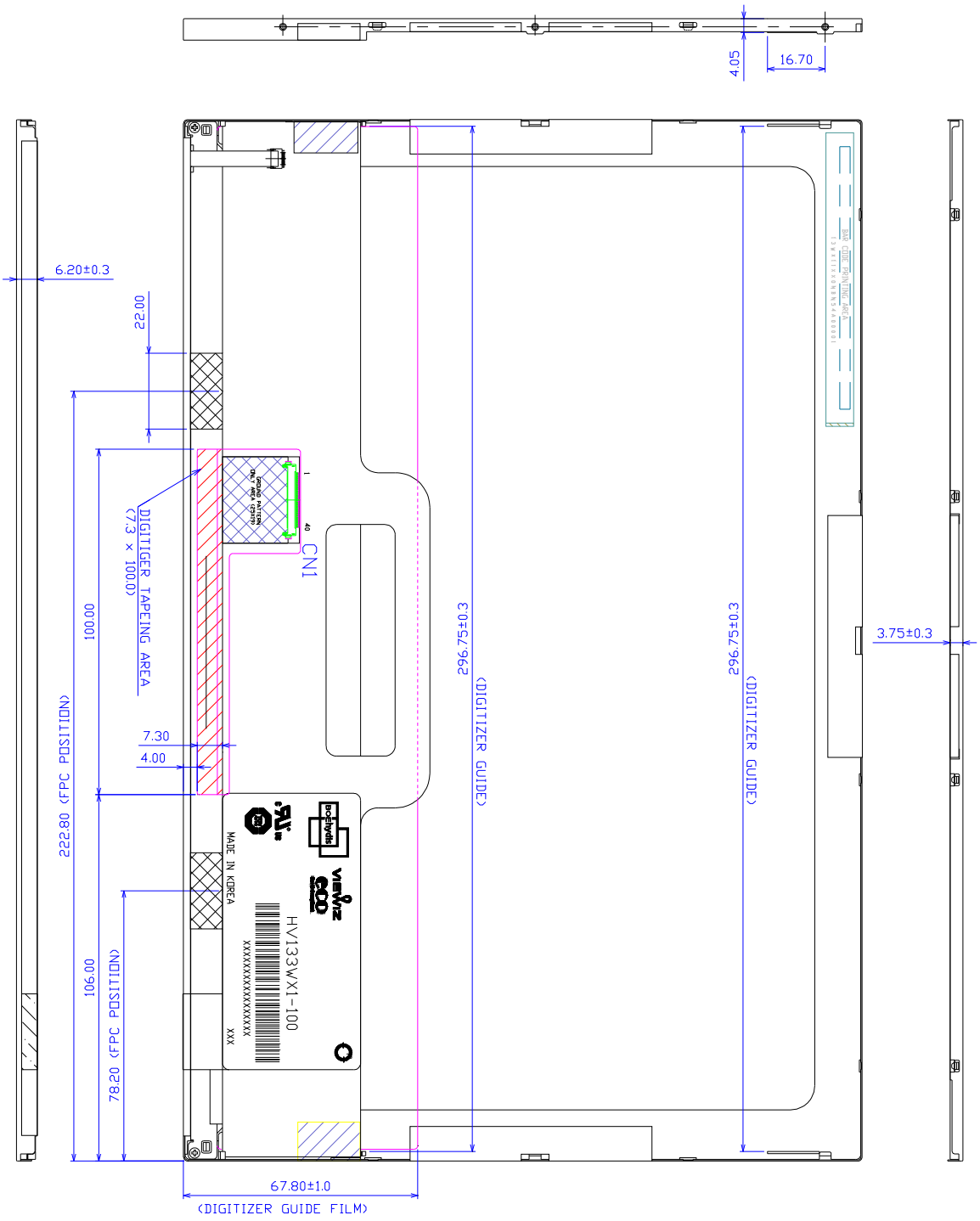
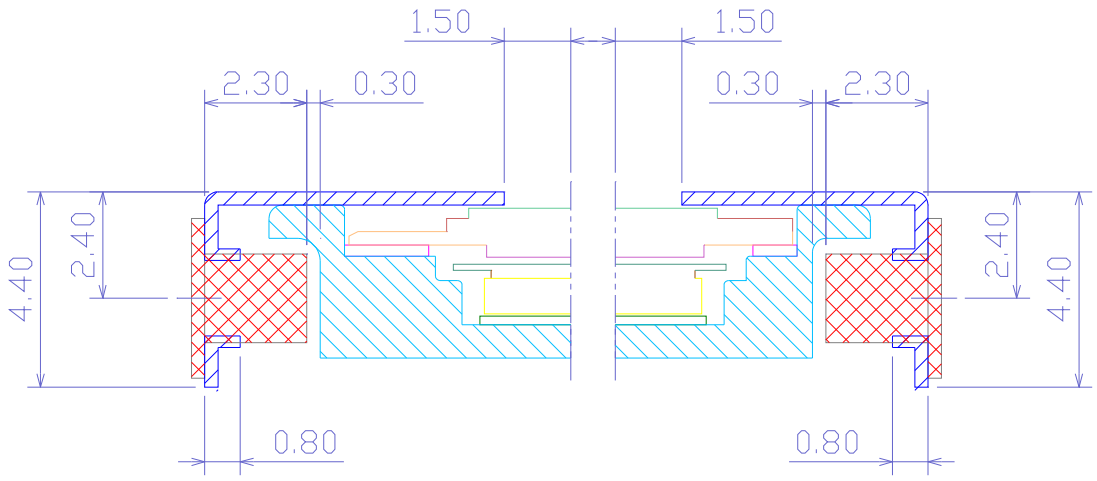
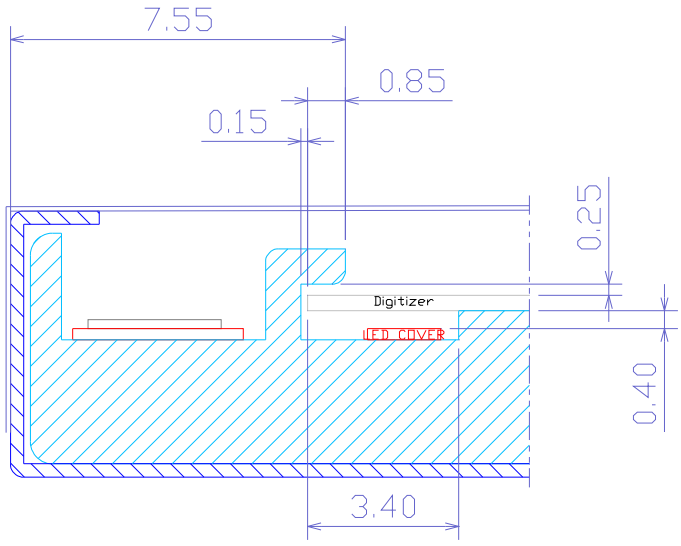


**Figure 8. TFT-LCD Module Section for Mounting**




SECTION A-A(S:5/1)

SECTION B-B(S:5/1)




SECTION C-C(S:5/1)

## 12.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

<Table 12. Reliability Test>

| No | Test Item | Conditions |
|---|---|---|
| 1 | High temperature storage test | Ta = 60 °C, 240 hrs |
| 2 | Low temperature storage test | Ta = -20 °C, 240 hrs |
| 3 | High temperature & high humidity operation test | Ta = 50 ℃, 80%RH, 240hrs |
| 4 | High temperature operation test | Ta = 50 °C, 240 hrs |
| 5 | Low temperature operation test | Ta = 0 °C, 240 hrs |
| 6 | Thermal shock | Ta = -20 °C ↔ 60 °C (30 min), 100 cycle |
| 7 | Vibration test (non-operating) | Frequency : 10~500Hz<br>Gravity/AMP : 1.5G<br>Period : X,Y,Z 30min |
| 8 | Shock test (non-operating) | Gravity : 220G<br>Pulse width : 2ms, half sine wave<br>±X, ±Y, ±Z Once for each direction |
| 9 | Electro-static discharge test (non-operating) | Air : 150pF, 330ohm, 15KV<br>Contact : 150pF, 330ohm, 8KV |

## 13.0 HANDLING & CAUTIONS

### 13.1 Cautions when taking out the module

- Pick the pouch only, when taking out module from a shipping package.

### 13.2 Cautions for handling the module

- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and back - light element are made from fragile glass (epoxy) material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables with care.

**13.3 Cautions for the operation**

- When the module is operating, do not lose MCLK, DE signals. If any one of these signals were lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

**13.4 Cautions for the atmosphere**

- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

**13.5 Cautions for the module characteristics**

- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

**13.6 Cautions for the digitizer assembly**

- When assembling FPC connector, do not flip connector past 90° due to possible damage to connector.
- When positioning digitizer underneath driver IC, do not lift driver IC past 90° due to possible damage to drive IC pattern.
- Please be warned that during assembly of digitizer, the opening or closing of FPC will result in possible electrostatic discharge damage to the LED

**13.7 Other cautions**

- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

# 14.0 LABELS

## 14.1 Product Label

**BOE HYDIS Barcode**

| 1 | 2 | 3 | 4 | 5 | 6 | 7 |
|---|---|---|---|---|---|---|
| X X | X | X | X X | X | X X X X | X X X X X X |

No 1. Control Number

No 2. Rank / Grade

No 3. Line Classification
(BOE HYDIS : H, LCM : L, BOE OT : A/B/C)

No 4. Year (7 : 2007, 8 : 2008, …)

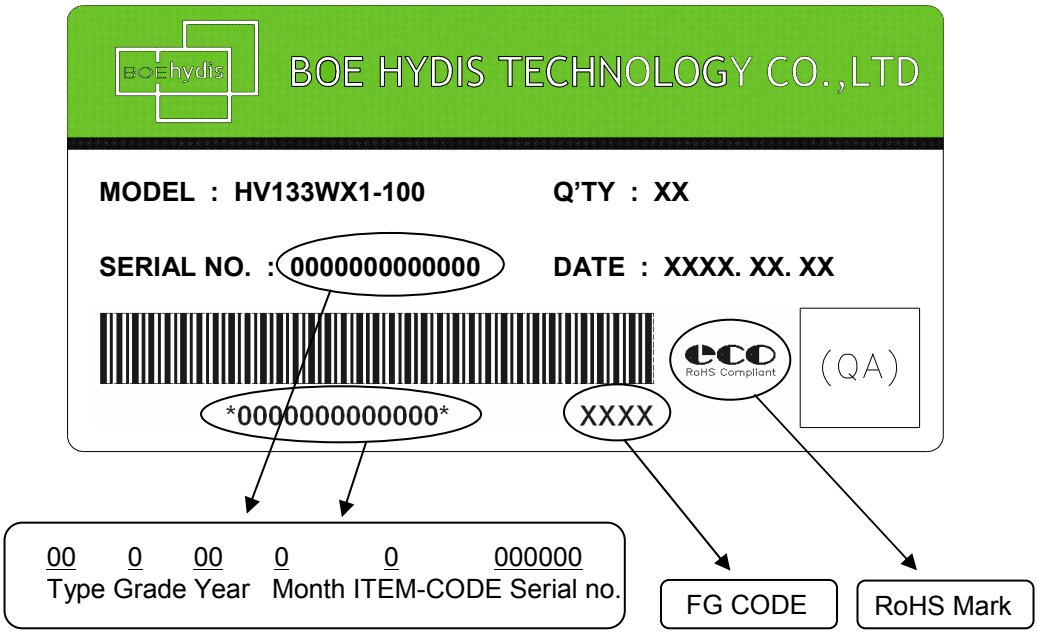
No 5. Month (1, 2, 3,…, 9, X, Y, Z)

No 6. FG Code

No 7. Serial Number

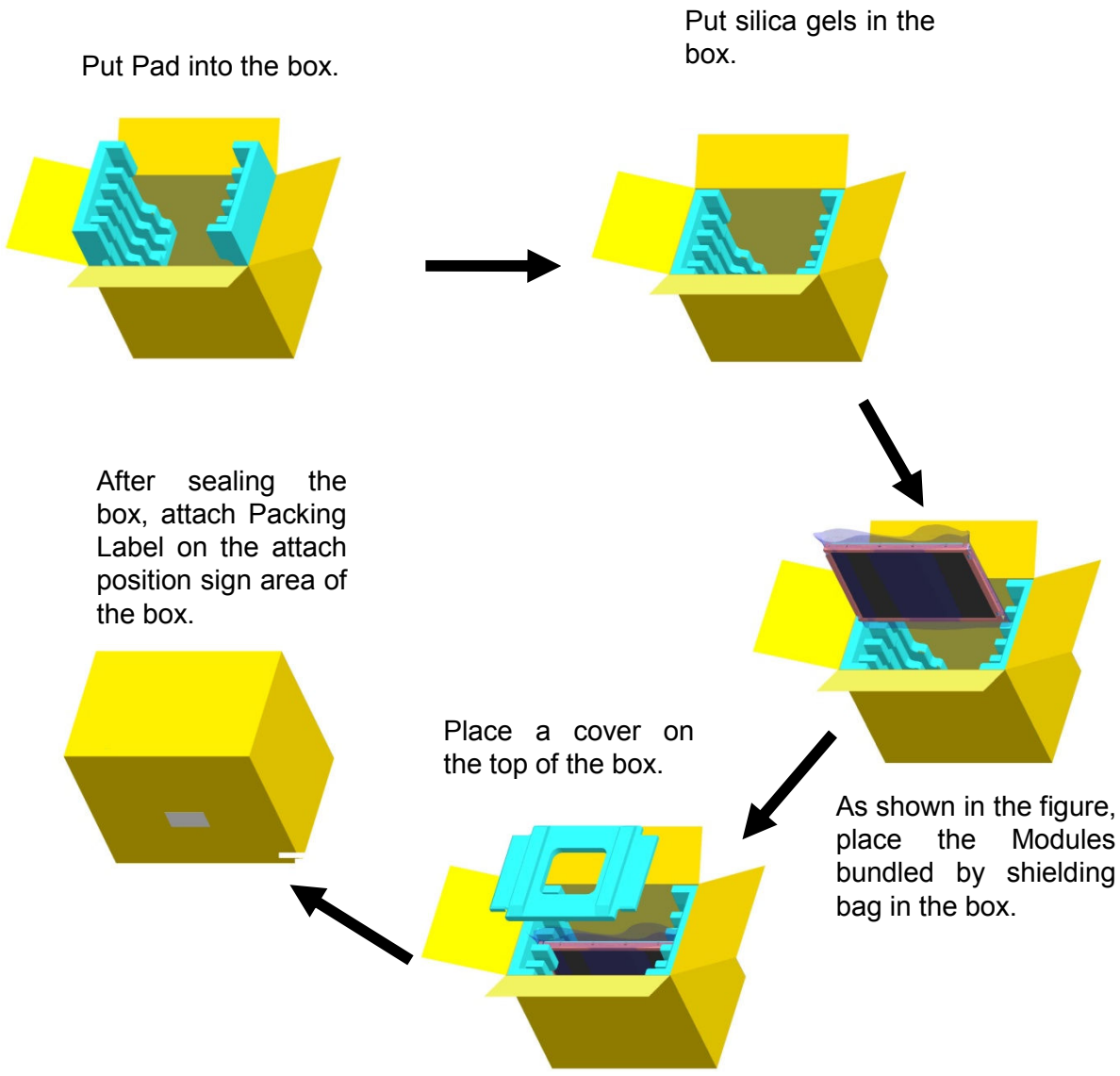
### 14.2 Packing Label

Label Size: 108 mm (L) × 56 mm (W)
Contents
Model: HV133WX1-100
Q`ty: Module Q`ty in one box
Serial No.: Box Serial No. See next figure for detail description.
Date: Packing Date
FG Code: FG Code of Product



# 15.0 PACKING INFORMATION

## 15.1 Packing order




## 15.2 Notes

- Box Dimension : 333.0mm(W) X 333.0mm(D) X 435.0mm(H)
- Package Quantity in one Box : 10pcs

## 16.0 EDID Table

EDID chip is 24LC024 (Microchip) or equivalent .

| Address (HEX) | Function | Hex | Dec | values. | Notes |
|---|---|---|---|---|---|
| 00 | Header | 00 | 0 | | EDID Header |
| 01 | | FF | 255 | | |
| 02 | | FF | 255 | | |
| 03 | | FF | 255 | | |
| 04 | | FF | 255 | | |
| 05 | | FF | 255 | | |
| 06 | | FF | 255 | | |
| 07 | | 00 | 0 | | |
| 08 | ID Manufacturer Name | 09 | 9 | BOE | ID = BOE |
| 09 | | E5 | 229 | | |
| 0A | ID Product Code | 28 | 40 | 2600 | CODE = 2600 |
| 0B | | 0A | 10 | | |
| 0C | 32-bit serial No. | 00 | 0 | | |
| 0D | | 00 | 0 | | |
| 0E | | 00 | 0 | | |
| 0F | | 00 | 0 | | |
| 10 | Week of manufacture | 0 | 0 | | |
| 11 | Year of Manufacture | 11 | 17 | 2007 | Manufactured in 2007 |
| 12 | EDID Structure Ver. | 01 | 1 | 1 | EDID Ver 1.0 |
| 13 | EDID revision # | 03 | 3 | 3 | EDID Rev. 0.3 |
| 14 | Video input definition | 80 | 128 | | |
| 15 | Max H image size | 1D | 29 | 29 | 29 cm (Approx) |
| 16 | Max V image size | 12 | 18 | 18 | 18 cm (Approx) |
| 17 | Display Gamma | 78 | 120 | 2.2 | Gamma curve = 2.2 |
| 18 | Feature support | 0A | 10 | | RGB display, Preferred Timming mode |
| 19 | Red/Green low bits | 6F | 111 | | Red / Green Low Bits |
| 1A | Blue/White low bits | 8C | 140 | | Blue / White Low Bits |
| 1B | Red x high bits | 90 | 144 | 0.563 | Rx = 0.563 |
| 1C | Red y high bits | 5A | 90 | 0.354 | Ry = 0.354 |
| 1D | Green x high bits | 54 | 84 | 0.331 | Gx = 0.331 |
| 1E | Green y high bits | 8A | 138 | 0.542 | Gy = 0.542 |
| 1F | Blue x high bits | 25 | 37 | 0.146 | Bx = 0.146 |
| 20 | BLue y high bits | 1E | 30 | 0.117 | By = 0.117 |
| 21 | White x high bits | 4C | 76 | 0.300 | Wx = 0.300 |
| 22 | White y high bits | 52 | 82 | 0.320 | Wy = 0.320 |

| Address (HEX) | Function | Hex | Dec | values. | Notes |
|---|---|---|---|---|---|
| 23 | Established timing 1 | 00 | 0 | | |
| 24 | Established timing 2 | 00 | 0 | | |
| 25 | Established timing 3 | 00 | 0 | | |
| 26 | Standard timing #1 | 01 | 1 | | Not Used |
| 27 | | 01 | 1 | | |
| 28 | Standard timing #2 | 01 | 1 | | Not Used |
| 29 | | 01 | 1 | | |
| 2A | Standard timing #3 | 01 | 1 | | Not Used |
| 2B | | 01 | 1 | | |
| 2C | Standard timing #4 | 01 | 1 | | Not Used |
| 2D | | 01 | 1 | | |
| 2E | Standard timing #5 | 01 | 1 | | Not Used |
| 2F | | 01 | 1 | | |
| 30 | Standard timing #6 | 01 | 1 | | Not Used |
| 31 | | 01 | 1 | | |
| 32 | Standard timing #7 | 01 | 1 | | Not Used |
| 33 | | 01 | 1 | | |
| 34 | Standard timing #8 | 01 | 1 | | Not Used |
| 35 | | 01 | 1 | | |
| 36 | Detailed timing/monitor descriptor #1 (60Hz) | C7 | 199 | 71.11 | 71.11MHz Main clock |
| 37 | | 1B | 27 | | |
| 38 | | 00 | 0 | 1280 | Hor Active = 1280 |
| 39 | | A0 | 160 | 160 | Hor Blanking = 160 |
| 3A | | 50 | 80 | | 4 bits of Hor. Active + 4 bits of Hor. Blanking |
| 3B | | 20 | 32 | 800 | Ver Active = 800 |
| 3C | | 17 | 23 | 23 | Ver Blanking = 23 |
| 3D | | 30 | 48 | | 4 bits of Ver. Active + 4 bits of Ver. Blanking |
| 3E | | 30 | 48 | 48 | Hor Sync Offset = 48 |
| 3F | | 20 | 32 | 32 | H Sync Pulse Width = 32 |
| 40 | | 36 | 54 | 3 | V sync Offset = 3 line |
| 41 | | 00 | 0 | 6 | V Sync Pulse width : 6 line |
| 42 | | 1E | 5 | 286 | Horizontal Image Size = 286 mm (Low 8 bits) |
| 43 | | B3 | 163 | 179 | Vertical Image Size = 179 mm (Low 8 bits) |
| 44 | | 10 | 16 | | 4 bits of Hor Image Size + 4 bits of Ver Image Size |
| 45 | | 00 | 0 | | Hor Border (pixels) |
| 46 | | 00 | 0 | | Vertical Border (Lines) |
| 47 | | 19 | 25 | | |

| Address (HEX) | Function | Hex | Dec | values. | Notes |
|---|---|---|---|---|---|
| 48 | Detailed timing/monitor descriptor #2 | 00 | 0 | | |
| 49 | | 00 | 0 | | |
| 4A | | 00 | 0 | | |
| 4B | | FE | 254 | | |
| 4C | | 00 | 0 | | |
| 4D | | 0A | 10 | | |
| 4E | | 20 | 32 | | |
| 4F | | 20 | 32 | | |
| 50 | | 20 | 32 | | |
| 51 | | 20 | 32 | | |
| 52 | | 20 | 32 | | |
| 53 | | 20 | 32 | | |
| 54 | | 20 | 32 | | |
| 55 | | 20 | 32 | | |
| 56 | | 20 | 32 | | |
| 57 | | 20 | 32 | | |
| 58 | | 20 | 32 | | |
| 59 | | 20 | 32 | | |
| 5A | Detailed timing/monitor descriptor #3 | 00 | 0 | | |
| 5B | | 00 | 0 | | |
| 5C | | 00 | 0 | | |
| 5D | | FE | 254 | | |
| 5E | | 00 | 0 | | |
| 5F | | 42 | 66 | B | |
| 60 | | 4F | 79 | O | |
| 61 | | 45 | 69 | E | |
| 62 | | 20 | 32 | | |
| 63 | | 48 | 72 | H | |
| 64 | | 59 | 89 | Y | |
| 65 | | 44 | 68 | D | |
| 66 | | 49 | 73 | I | |
| 67 | | 53 | 83 | S | |
| 68 | | 0A | 10 | | |
| 69 | | 20 | 32 | | |
| 6A | | 20 | 32 | | |
| 6B | | 20 | 32 | | |

| Address (HEX) | Function | Hex | Dec | values. | Notes |
|---|---|---|---|---|---|
| 6C | Detailed timing/monitor descriptor #4 | 00 | 0 | | Product Name Tag (ASCII) |
| 6D | | 00 | 0 | | |
| 6E | | 00 | 0 | | |
| 6F | | FE | 254 | | |
| 70 | | 00 | 0 | | |
| 71 | | 48 | 72 | H | Model name : HV133WX1 |
| 72 | | 56 | 86 | V | |
| 73 | | 31 | 49 | 1 | |
| 74 | | 33 | 51 | 3 | |
| 75 | | 33 | 51 | 3 | |
| 76 | | 57 | 87 | W | |
| 77 | | 58 | 88 | X | |
| 78 | | 31 | 49 | 1 | |
| 79 | | 20 | 32 | | |
| 7A | | 20 | 32 | | |
| 7B | | 20 | 32 | | |
| 7C | | 20 | 32 | | |
| 7D | | 0A | 10 | | |
| 7E | Extension flag | 00 | 0 | | |
| 7F | Checksum | 2F | 09 | | |